



Dimitrios P. BOURAS

ICT, IoT Expert

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Introduction

I am an information and communication systems engineer with nearly two decades of experience in the industry, on system architecture and hardware / software integration for a variety of successful products in both consumer and infrastructure sectors. Prior to joining Alpha Technologies as Software Development Manager, I have served as Chief Architect and acting VP Engineering at mimik Technology, and held engineering-lead positions at Thenamaris – a top-tier ships-management company, Atmel (now part of Microchip Technology), Theta Microelectronics (RFIC IP and design services) and Ericsson. For more than a decade I also served as external evaluator and reviewer for the European Commission, in the Information, Communication and IoT Technologies area, for projects under the EU 6th, 7th Framework (FP6, FP7) and Horizon 2020 Programs. During the course of nearly twelve years I co-monitored successful execution of fourteen projects, with budgets totalling in excess of 75M EUR and typical life-cycles of 2-4 years each. I hold postgraduate degrees in Digital Telecommunications, Electrical Engineering, from the University of British Columbia, Vancouver, Canada.

Employment experience

Software Development Manager

June 2017 – present

Alpha Technologies Ltd.

Defining the strategy and delivering software for embedded firmware, intelligent system controllers and IoT components for industrial power solutions. Working with customers, product managers and sales for developing product road-maps. Reporting to the VP of Product Management and Development and advising the executive team on “build, buy, partner and avoid decisions”. Collaborating with program managers, engineers, technologists, finance, supply chain, manufacturing, technical support and documentation.

Chief Architect / acting VP Engineering

May 2015 – April 2017

mimik Technology Inc.

Responsible for leading an agile engineering team in a pre-revenue startup environment, providing architectural direction and coordinating implementation plans for an edge-computing micro-service based cloud platform. Since joining I was instrumental in revamping software development life-cycle management tools and processes, contributing to the transformation of an R&D culture into a product and market driven one. As a senior member of the management team, I regularly communicated company technology strategy to employees, partners, customers and investors.

Independent Expert (Evaluator, Reviewer)

November 2003 – present

European Commission, DG INFSO & DG Connect

Since November 2003 I have been serving the European Commission, in the Information, Communication and IoT Technologies area, as evaluator and rapporteur during selection of projects for funding, as well as reviewer for monitoring execution of funded projects, under the EU 6th, 7th Framework (FP6, FP7) and Horizon 2020 R&D Programs. Over the course of nearly twelve years, I took part in a total of 10 week-long evaluation sessions and co-monitored successful execution of 14 projects with budgets totalling in excess of 76M EUR and typical life-cycles of 2-4 years each.

Electrical Eng. Supervisor

December 2010 – March 2015

Thenamaris Ships Management Inc.

Developing and leading the electrical engineering group within the Technical Department. The group supports superintendent engineers, fleet electricians (in the order of one hundred individuals) and the new-buildings team in all things related to electronic automation, on-board ICT, telecommunications, radio navigation and telemetry for energy optimization. Responsible for overseeing maintenance, as well as planning, management and tracking of related development and infrastructure projects with average annual budgets of 1-2M USD.

Manager, Systems & Applications

September 2006 – July 2010

Theta Microelectronics Inc.

Leading the Systems & Applications Group that served as interface between customer technical requirements or specifications, established or emerging standards and internal R&D / implementation effort, guiding design decisions based on capabilities and trade-offs. Successfully executed projects: RFICs for broadband wireless (MIMO) distribution of HDTV signals in the home; fully integrated RFIC transceivers for microwave back-haul networks. Also served as company interface to our customers, managing interaction and chairing review meetings.



Employment Experience (cont.)

Principal System Engineer

October 1998 – August 2006

Atmel Corp. (now part of Microchip Technology Inc.)

Managing the digital signal processing team, designing and implementing ASIC hardware as well as developing firmware for telecom systems. Designing and implementing means of assessing quality of team output. Major project engagements were in state of the art wireless topics such as WiFi (IEEE-802.11a/b/g wireless LAN), WiMax (IEEE-802.16 wireless MAN) and BlueTooth (IEEE-802.15 wireless PAN), both as senior design engineer and as group manager.

LANGUAGES

ENGLISH

Native / bilingual proficiency

GREEK

Native / bilingual proficiency

FRENCH

Limited working proficiency

INTERESTS

Wireless Technology

Transceivers, antennas, analog and digital systems
Amateur radio qualifications: Basic, Advanced, Morse code (call-signs VE7HDB / SV0XCB)

Computer Software / Hardware

Involvement with computing and networking reaches back to the mid 90's and the infancy of the Internet and the World Wide Web. Experimentation with operating systems spans nearly three decades, beginning with Exec 8 on a Sperry Univac 1100 series mainframe and Unix on a Perkin Elmer 3200 series minicomputer. Contributing code and advocating software freedom for over twenty years, as an earnest supporter of the Open Source movement since its inception

Outdoors

Hiking, Skiing (down-hill & water), Sailing, Biking

Music

Classical & electric guitar; five years of conservatory instruction between ages 5-10

Education, Honours & Awards

Ph.D. Electrical Eng. – Telecommunications

1991 – 1995

The University of British Columbia, Vancouver, Canada

Thesis Title: "Advanced Noncoherent Receivers for Mobile Fading Channels" (2 years Research Assistantship, 2 years University Graduate Fellowship). Recipient of a UBC University Graduate Fellowship (1993-1995). University-wide "Best Ph.D. Thesis" nomination for the Natural Sciences and Engineering Research Council of Canada (NSERC) award for 1995.

M.A.Sc. Electrical Eng. – Telecommunications

1989 – 1991

The University of British Columbia, Vancouver, Canada

Thesis Title: "Optimal Decoding of PSK and QAM Signals in Frequency Nonselective Fading Channels" (1.5 years Research Assistantship).

Diploma Electrical Eng.

1983 – 1989

University of Patras, Patras, Greece

Five-year study including thesis, specialization field: telecommunications.

Professional Organizations

Institute of Electrical & Electronics Engineers (IEEE)

1989 – present

Societies

IEEE Communications Society
IEEE Vehicular Technology Society

IEEE Wireless Rural & Emergency Communications Conference (WRECOM) 2007

Technical Program Committee member

IEEE Vehicular Technology Conference (VTC) Fall 2008

Technical Program Committee member

IEEE Vehicular Technology Conference (VTC) Spring 2009

Technical Program Committee member

Volunteering & Causes

UBC Amateur Radio Society

1991 – 1995

Member, President, Alumni member

Non-profit, student organization governed by UBC's Alma Mater Society, with a mission to divulge Amateur Radio as a hobby to students, faculty, staff and the community. During the years of my involvement we greatly increased student membership, successfully completed ambitious infrastructure projects and published two preparatory course books (of which I am a co-author) on obtaining an Amateur Radio Operator certificate.

Organizations

Free Software Foundation, Associate Member

2006 – present

The Planetary Society, Member

1992 – present

Wikimedia Foundation,

The Document Foundation,

Canadian Red Cross